

MTech VLSI					
S2_Mtech					
Cat	Code	Title	L T P	Cr	ES
SC	18VL614	Design Verification	3 0 0	3	A
SC	18VL615	VLSI Signal Processing	3 0 0	3	B
SC	18VL616	Design for Test and Testing	3 0 0	3	C
E		Fractal Elective I	1 0 0	1	
	18VL771	Analog Layout			D
E		Fractal Elective II	1 0 0	1	
	18VL772	Feedback Amplifiers			E
E		Fractal Elective III	1 0 0	1	
	18VL773	Analog Filters			H
E		Live-in Lab/ Elective I	3 0 0	3	I
	18VL723	Emerging Architectures for Machine Learning			
E		Elective II/ Open Elective**	3 0 0	3	G
	18CE612	RF Circuits and Systems			
	18VL762	VLSI Fabrication Technology			
SC	18VL632	VLSI Design Laboratory –II	0 0 2	1	L1
SC	18RM600	Research Methodology	2 0 2	3	F
HU	18HU603	Career Competency II	0 0 2	1	L2

Fourth Semester

	Code	Course	Cr
	16VL799	Dissertation	10
		Credits	10

L- Lecture; T-Tutorial; P-Practical